

<p>Substitute for form 1449A/PTO</p> <p><b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b></p> <p>(use as many sheets as necessary)</p>				<p><i>Complete if Known</i></p> <table border="1"> <tr> <td>Application Number</td> <td>09/849658</td> </tr> <tr> <td>Filing Date</td> <td>5/4/2001</td> </tr> <tr> <td>First Named Inventor</td> <td>G. Glenn Henry</td> </tr> <tr> <td>Group Art Unit</td> <td>2181</td> </tr> <tr> <td>Examiner Name</td> <td>Tonia Meonske</td> </tr> <tr> <td>Attorney Docket Number</td> <td>CNTR.2022</td> </tr> </table>		Application Number	09/849658	Filing Date	5/4/2001	First Named Inventor	G. Glenn Henry	Group Art Unit	2181	Examiner Name	Tonia Meonske	Attorney Docket Number	CNTR.2022
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## U.S. PATENT DOCUMENTS

Examiner Initials*	Cite No. <sup>1</sup>	Document Number		Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind	Code <sup>2</sup> (known)			
TM		0988444		11-22-2006	Kroesche et al.	
TM		6725357		04-20-2004	Jean Philippe Cousin	
TM		6041408		03-21-2000	Thomas S. Green	
TM		6754808		08-22-2004	Roth et al.	
TM		6374350		04-16-2002	DSa et al	
TM		5434985		07-18-1998	Emma et al	
TM		5553248		09-03-1998	Hiromi Suzuki	
TM		6122729		08-19-2000	Thang M. Tran	
TM		6748441		08-08-2004	David James Gemmell	

## NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
TM	AA	The D Latch, Play-Hockey Web Page, Oct 10, 1999.	-
TM	AB	Patterson et al. "Computer Organization & Design: The Hardware/Software Interface." Morgan Kaufmann Publishers, Inc.	-
		San Francisco, CA. 1988 Page 469 <u>SECOND EDITION</u>	-
TM	AC	Jimenez et al. "The Impact of Delay on the Design of Branch Predictors." 2000 <u>IEEE</u> PES 1-10	-
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**Examiner  
Signature**

Conrad P. Neenstra

**Date  
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June 12, 2006

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TM	AA	EBERLY et al. "The Correlation Branch Target Address Cache" May 1998, P 8583-85, VOL. 39	ET7
TM	AB	Patterson et al. "Computer Organization & Design: The Hardware/Software Interface." Morgan Kaufmann Publishers, Inc. San Francisco, CA. 1998 Page 463-466 <u>SECOND EDITION</u>	

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